

Class Exercise I

1. Make a diagram showing successive bytes of memory like those above to show the memory layout produces by the following data declarations:

```

Letters      DB    'ABC'
Digits       DB    1, 2, 3
Numbers      DW    6767h, 0abbah    ;h indicates hex
More         DB    'e', 10, 'fg'
Hush         DB    5 DUP ('S'), 'H!'
Two3         DB    3 DUP (2, 3, ?)
Recurse      DB    2 DUP ('X', 3 DUP (0))

```

(Hint : the '0' in 0abbah is merely to indicate to the assembler that a number follows.)

2. Given the following data definitions:

```

W          DW    1234
A          DB    23
B          DB    -12

```

which of the following **mov** instructions are illegal and why?

```

mov    W, 74
mov    al, B
mov    74, W
mov    A, 74
mov    W, ah
mov    A, B

```

3. Fill the contents of the specified registers as four hex digits, given the data definitions.

```

AWord      DW    5432h
AByte      DB    9ah
Another     DB    0bch

```

```

mov    ax, 1234h;      ax = 1234h

mov    ax, AWord;      ax = 5432h
mov    ax, 1234h;
mov    ah, AByte;      ax = 9a34h
mov    ah, AByte ;
mov    al, Another;    ax = 9abch
mov    ax, 1234h;
mov    al, ah;          ax = 1212h
mov    ax, 1234h;
mov    ah, 'A';         ax = 4134h      (HINT: 'A' = 65 = 41h)
mov    ax, 1234h;
mov    ax, 'A';         ax = 0041h
mov    ah, 1;           ax = 0102h
mov    al, 2;

```

